

Amendments to the specification

Amend the paragraph beginning at page 10, line 4, as indicated below:

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a-1 Fig. 3 illustrates the circuit configurations of and around buffer amplifiers 14 and the ladder resistor 15. The ladder resistor 15 is provided with nine external circuit connection terminals 16, to each of which is connected the output of a reference voltage generating amplifier 18 of a reference voltage generating circuit 17, which is a Si-large scale integrated circuit (Si-LSI). The ladder resistor 15 has, among respective ~~between~~ every two external circuit connection terminals 16, eight buffer amplifiers 14, whose outputs are connected to reference voltage lines 8. There are altogether 64 buffer amplifiers 14, and this number matches the aforementioned six-bit structure of the display data.

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